

Day : Friday
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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = THAKKAR

First Name = SHREEKANT

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60198227</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
<u>60198226</u>	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
<u>60198225</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S
<u>10663169</u>	Not Issued	019	09/15/2003	EXTENDED STAND COMPUTER SYSTEM WITH RETRACTABLE KEYBOARD	THAKKAR, SHREEKANT SURYAKANT
<u>10663013</u>	Not Issued	019	09/15/2003	COMPUTER SYSTEM WITH DETACHABLE DISPLAY	THAKKAR, SHREEKANT SURYAKANT
<u>10367378</u>	Not Issued	041	02/14/2003	CONVERTIBLE AND DETACHABLE LAPTOPS	THAKKAR, SHREEKANT SURYAKANT
<u>10367364</u>	Not Issued	030	02/14/2003	CONNECTION ARRANGEMENT FOR A PEN-BASED COMPUTING SYSTEM	THAKKAR, SHREEKANT SURYAKANT
<u>10164976</u>	Not Issued	095	06/06/2002	METHOD AND APPARATUS FOR STAGGERING EXECUTION OF A SINGLE PACKED DATA INSTRUCTION USING THE SAME CIRCUIT	THAKKAR, SHREEKANT S.
<u>10164774</u>	Not Issued	095	06/06/2002	METHOD AND APPARATUS FOR STAGGERING EXECUTION OF A SINGLE PACKED DATA INSTRUCTION USING THE SAME CIRCUIT	THAKKAR, SHREEKANT S.

<u>10057704</u>	Not Issued	030	01/24/2002	CONTEXT-BASED INFORMATION PROCESSING	THAKKAR, SHREEKANT S.
<u>10057163</u>	Not Issued	019	01/24/2002	CONTEXT-BASED INFORMATION PROCESSING	THAKKAR, SHREEKANT S.
<u>09852217</u>	Not Issued	092	05/08/2001	EXECUTING PARTIAL-WIDTH PACKED DATA INSTRUCTIONS	THAKKAR, SHREEKANT S.
<u>09845610</u>	<u>6502115</u>	150	04/27/2001	CONVERSION BETWEEN PACKED FLOATING POINT DATA AND PACKED 32-BIT INTEGER DATA IN DIFFERENT ARCHITECTURAL REGISTERS	THAKKAR, SHREEKANT S.
<u>09844728</u>	<u>6480868</u>	150	04/27/2001	CONVERSION FROM PACKED FLOATING POINT DATA TO PACKED 8-BIT INTEGER DATA IN DIFFERENT ARCHITECTURAL REGISTERS	THAKKAR, SHREEKANT S.
<u>09823131</u>	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	THAKKAR, SHREEKANT S.
<u>09822986</u>	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
<u>09805280</u>	<u>6425073</u>	150	03/13/2001	METHOD AND APPARATUS FOR STAGGERING EXECUTION OF AN INSTRUCTION	THAKKAR, SHREEKANT S.
<u>09783875</u>	Not Issued	160	02/14/2001	METHOD AND APPARATUS FOR FLOATING POINT OPERATIONS AND FORMAT CONVERSION OPERATIONS	THAKKAR, SHREEKANT S.
<u>09783779</u>	Not Issued	041	02/14/2001	SHUFFLE INSTRUCTIONS	THAKKAR, SHREEKANT S.
<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
<u>09748813</u>	<u>6622087</u>	150	12/26/2000	METHOD AND APPARATUS FOR DERIVING TRAVEL PROFILES	THAKKAR, SHREEKANT
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	THAKKAR, SHREEKANT S.

<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	THAKKAR, SHREEKANT S.
<u>09668610</u>	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	THAKKAR, SHREEKANT S.
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	THAKKAR, SHREEKANT S.
<u>09618738</u>	<u>6678825</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	THAKKAR, SHREEKANT S.
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
<u>09608982</u>	Not Issued	030	06/30/2000	DIGITAL WATERMARKS WITH VALUES DERIVED FROM REMOTE PLATFORMS	THAKKAR, SHREEKANT S.
<u>09566620</u>	Not Issued	041	05/08/2000	PROVIDING INFORMATION TO A COMMUNICATIONS DEVICE	THAKKAR, SHREEKANT S.
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	THAKKAR, SHREEKANT S.
<u>09541477</u>	<u>6507904</u>	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	THAKKAR, SHREEKANT S
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	THAKKAR, SHREEKANT S.
<u>09540946</u>	Not	030	03/31/2000	PROTECTING SOFTWARE	THAKKAR,

	Issued			ENVIRONMENT IN ISOLATED EXECUTION	SHREEKANT S.
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	THAKKAR, SHREEKANT S.
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	THAKKAR, SHREEKANT S.
<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
<u>09539344</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	THAKKAR, SHREEKANT S.
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	THAKKAR, SHREEKANT S.
<u>09223302</u>	<u>6463494</u>	150	12/30/1998	METHOD AND SYSTEM FOR IMPLEMENTING CONTROL SIGNALS ON A LOW PIN COUNT BUS	THAKKAR , SHREEKANT
<u>09176571</u>	<u>6434650</u>	150	10/21/1998	APPARATUS AND METHOD FOR MULTIPLEXING BI-DIRECTIONAL DATA ONTO A LOW PIN COUNT BUS BETWEEN A HOST CPU AND A CO-PROCESSOR	THAKKAR , SHREEKANT
<u>09170136</u>	<u>6185670</u>	150	10/12/1998	SYSTEM FOR REDUCING NUMBER OF OPCODES	THAKKAR , SHREEKANT S.

				REQUIRED IN A PROCESSOR USING AN INSTRUCTION FORMAT INCLUDING OPERATION CLASS CODE AND OPERATION SELECTOR CODE FIELDS	
<u>09122349</u>	Not Issued	041	07/24/1998	METHOD AND APPARATUS FOR PERFORMING CACHE SEGMENT FLUSH AND CACHE SEGMENT INVALIDATION OPERATIONS	THAKKAR , SHREEKANT
<u>09052881</u>	<u>6052769</u>	150	03/31/1998	METHOD AND APPARATUS FOR MOVING SELECT NON-CONTIGUOUS BYTES OF PACKED DATA IN A SINGLE INSTRUCTION	THAKKAR , SHREEKANT
<u>09049827</u>	<u>6317824</u>	150	03/27/1998	METHOD AND APPARATUS FOR PERFORMING INTEGER OPERATIONS IN RESPONSE TO A RESULT OF A FLOATING POINT OPERATION	THAKKAR , SHREEKANT
<u>08914578</u>	<u>5946713</u>	150	08/18/1997	MEMORY ATTRIBUTE PALETTE	THAKKAR , SHREEKANT S.
<u>08579932</u>	<u>5724527</u>	150	12/28/1995	FAULT-TOLERANT BOOT STRAP MECHANISM FOR A MULTIPROCESSOR SYSTEM	THAKKAR , SHREEKANT

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Day : Friday
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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = SUTTON

First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>60491832</u>	Not Issued	020	08/01/2003	ADAMANTYLGLYCINE-BASED INHIBITORS OF DIPEPTIDYL PEPTIDASE IV AND METHODS	SUTTON, JAMES C.
<u>60434060</u>	Not Issued	020	12/17/2002	BETA LACTAM COMPOUNDS AND THEIR USE AS INHIBITORS OF TRYPTASE	SUTTON, JAMES C.
<u>60364596</u>	Not Issued	159	03/18/2002	ELECTRONIC DOCUMENT PRESENTMENT SERVICES IN THE EVENT OF A DISASTER	SUTTON, JAMES N.
<u>60361881</u>	Not Issued	159	03/06/2002	PULSE & RESPIRATION TIMER	SUTTON, JAMES R.
<u>60246392</u>	Not Issued	159	11/07/2000	ACID DERIVATIVES USEFUL AS SERINE PROTEASE INHIBITORS	SUTTON, JAMES C.
<u>60246391</u>	Not Issued	159	11/07/2000	ACID DERIVATIVES USEFUL AS SERINE PROTEASE INHIBITORS	SUTTON, JAMES C.
<u>60198227</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
<u>60198226</u>	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
<u>60198225</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
<u>10321736</u>	Not Issued	041	12/17/2002	HAND WINCH HANDLE WITH RELEASE FEATURE THAT PERMITS CABLE TO WIND OFF OF WINCH DRUM WITHOUT HANDLE ROTATION	SUTTON, JAMES A.
<u>10165597</u>	Not Issued	030	06/07/2002	SYSTEM AND METHOD FOR	SUTTON,

				PROTECTION AGAINST UNTRUSTED SYSTEM MANAGEMENT CODE BY REDIRECTING A SYSTEM MANAGEMENT INTERRUPT AND CREATING A VIRTUAL MACHINE CONTAINER	JAMES A.
<u>10123599</u>	Not Issued	030	04/15/2002	PROTECTION AGAINST MEMORY ATTACKS FOLLOWING RESET	SUTTON, JAMES A.
<u>10122785</u>	Not Issued	030	04/15/2002	VALIDATION OF INCLUSION OF A PLATFORM WITHIN A DATA CENTER	SUTTON, JAMES A.
<u>10122544</u>	Not Issued	030	04/15/2002	METHOD AND APPARATUS FOR COMMUNICATING SECURELY WITH A TOKEN	SUTTON, JAMES A.
<u>10121807</u>	Not Issued	030	04/12/2002	MICROCODE PATCH AUTHENTICATION	SUTTON, JAMES A.
<u>10116923</u>	Not Issued	030	04/05/2002	METHODS AND ARRANGEMENTS TO REGISTER CODE	SUTTON, JAMES A.
<u>10112169</u>	Not Issued	030	03/29/2002	SYSTEM AND METHOD FOR EXECUTION OF A SECURED ENVIRONMENT INITIALIZATION INSTRUCTION	SUTTON, JAMES A.
<u>10112124</u>	Not Issued	030	03/29/2002	SYSTEM AND METHOD FOR RESETTING A PLATFORM CONFIGURATION REGISTER	SUTTON, JAMES A.
<u>10085839</u>	Not Issued	030	02/25/2002	METHOD AND APPARATUS FOR LOADING A TRUSTABLE OPERATING SYSTEM	SUTTON, JAMES A.
<u>10052927</u>	<u>6642252</u>	150	11/07/2001	ACID DERIVATIVES USEFUL AS SERINE PROTEASE INHIBITORS	SUTTON, JAMES C.
<u>10043843</u>	Not Issued	030	11/01/2001	APPARATUS AND METHOD FOR UNILATERALLY LOADING A SECURE OPERATING SYSTEM WITHIN A MULTIPROCESSOR ENVIRONMENT	SUTTON, JAMES A.
<u>10041071</u>	Not Issued	030	12/28/2001	AUTHENTICATED CODE METHOD AND APPARATUS	SUTTON, JAMES A.
<u>10040872</u>	<u>6646846</u>	150	10/29/2001	APPARATUS AND METHOD FOR STAGED POWER LOAD TRANSFER	SUTTON, JAMES A.
<u>10039961</u>	Not Issued	030	12/31/2001	PROCESSOR SUPPORTING EXECUTION OF AN AUTHENTICATED CODE	SUTTON, JAMES A.

INSTRUCTION					
<u>10039595</u>	Not Issued	030	12/31/2001	AUTHENTICATED CODE MODULE	SUTTON, JAMES A.
<u>10035714</u>	Not Issued	164	11/07/2001	ACID DERIVATIVES USEFUL AS SERINE PROTEASE INHIBITORS	SUTTON, JAMES C.
<u>09942503</u>	Not Issued	030	08/29/2001	ACQUISITION AND ORGANIZATION OF DIGITAL IMAGES	SUTTON, JAMES E.
<u>09823131</u>	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	SUTTON, JAMES A.
<u>09822986</u>	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	SUTTON, JAMES A.
<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	SUTTON, JAMES A.
<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	SUTTON, JAMES A.
<u>09668610</u>	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	SUTTON, JAMES A.
<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	SUTTON, JAMES A.
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	SUTTON, JAMES A.
<u>09618738</u>	<u>6678825</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A	SUTTON, JAMES A.

				CLEANUP FLAG IS SET	
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
<u>09605605</u>	Not Issued	041	06/28/2000	PLATFORM AND METHOD FOR ESTABLISHING PROVABLE IDENTITIES WHILE MAINTAINING PRIVACY	SUTTON, JAMES A.
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	SUTTON, JAMES A.
<u>09541477</u>	<u>6507904</u>	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	SUTTON, JAMES A.
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	SUTTON, JAMES A.
<u>09540946</u>	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	SUTTON, JAMES A.
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	SUTTON, JAMES A.
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	SUTTON, JAMES A.
<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	SUTTON, JAMES A.
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	SUTTON, JAMES A.
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
<u>09539344</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	SUTTON, JAMES A.
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	SUTTON, JAMES A.
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR	SUTTON,

				ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	JAMES A.	
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Day : Friday
 Date: 1/9/2004
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Inventor Name Search Result

Your Search was:

Last Name = RENERIS

First Name = KEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60198227</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
<u>60198226</u>	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
<u>60198225</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
<u>09823131</u>	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	RENERIS, KEN
<u>09822986</u>	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	RENERIS, KEN
<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	RENERIS, KEN
<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	RENERIS, KEN
<u>09672367</u>	Not Issued	030	09/28/2000	MECHANISM TO SECURE COMPUTER OUTPUT FROM SOFTWARE ATTACK USING ISOLATED EXECUTION	RENERIS, KEN
<u>09668610</u>	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	RENERIS, KEN

<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	RENERIS, KEN
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	RENERIS, KEN
<u>09618738</u>	<u>6678825</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	RENERIS, KEN
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
<u>09561213</u>	Not Issued	161	04/28/2000	METHOD AND APPARATUS FOR REMOTELY DEBUGGING COMPUTER SOFTWARE OVER A SERIAL BUS	RENERIS, KENNETH S.
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	RENERIS, KEN
<u>09541477</u>	<u>6507904</u>	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	RENERIS, KEN
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	RENERIS, KEN
<u>09540946</u>	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	RENERIS, KEN
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	RENERIS, KEN
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	RENERIS, KEN

<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	RENERIS, KEN
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	RENERIS, KEN
<u>09539588</u>	<u>6243821</u>	150	03/31/2000	SYSTEM AND METHOD FOR MANAGING POWER CONSUMPTION IN A COMPUTER SYSTEM	RENERIS, KENNETH S.
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
<u>09539344</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	RENERIS, KEN
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	RENERIS, KEN
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	RENERIS, KEN
<u>09488015</u>	Not Issued	168	01/20/2000	METHOD AND APPARATUS FOR REMOTELY DEBUGGING COMPUTER SOFTWARE OVER A SERIAL BUS	RENERIS, KENNETH S.
<u>09257923</u>	<u>6526431</u>	150	02/26/1999	MAINTAINING EXTENDED AND TRADITIONAL STATES OF A PROCESSING UNIT IN TASK SWITCHING	RENERIS , KENNETH S.
<u>09158167</u>	<u>6209088</u>	150	09/21/1998	COMPUTER HIBERNATION IMPLEMENTED BY A COMPUTER OPERATING SYSTEM	RENERIS , KENNETH S.
<u>09098925</u>	<u>6092208</u>	150	06/17/1998	A SYSTEM AND METHOD FOR WAKING A COMPUTER HAVING A PLURALITY OF POWER RESOURCES FROM A SYSTEM STATE USING A DATA STRUCTURE	RENERIS , KENNETH S.
<u>08953933</u>	Not Issued	161	10/20/1997	OPERATING SYSTEM DEVICE CONFIGURATION MANAGER	RENERIS , KENNETH S.

AND METHOD					
<u>08831111</u>	<u>6343338</u>	150	04/01/1997	SYSTEM AND METHOD FOR SYNCHRONIZING DISPARATE PROCESSING MODES AND FOR CONTROLLING ACCESS TO SHARED RESOURCES	RENERIS , KENNETH S.
<u>08810019</u>	<u>5903894</u>	150	03/03/1997	SYSTEM AND METHOD FOR USING A HIERARCHICAL DATA STRUCTURE TO CONTROL AND IDENTIFY DEVICES AND REPRESENT CONNECTIONS BETWEEN THE DEVICES	RENERIS , KENNETH S.
<u>08810018</u>	<u>5919264</u>	150	03/03/1997	A SYSTEM AND METHOD FOR USING DATA STRUCTURES TO SHARE A PLURALITY OF POWER RESOURCES AMONG A PLURALITY OF DEVICES	RENERIS , KENNETH S.
<u>08614186</u>	<u>5784628</u>	150	03/12/1996	METHOD AND SYSTEM FOR CONTROLLING POWER CONSUMPTION IN A COMPUTER SYSTEM	RENERIS , KENNETH S.
<u>08428961</u>	Not Issued	166	04/24/1995	OPERATING SYSTEM DEVICE CONFIGURATION MANAGER AND METHOD	RENERIS , KENNETH S.

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Your Search was:

Last Name = NEIGER

First Name = GIL

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>60198227</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
<u>60198226</u>	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
<u>60198225</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
<u>10676890</u>	Not Issued	019	09/30/2003	CHIPSET SUPPORT FOR MANAGING HARDWARE INTERRUPTS IN A VIRTUAL MACHINE SYSTEM	NEIGER, GILBERT
<u>10676887</u>	Not Issued	019	09/30/2003	MECHANISM TO CONTROL HARDWARE INTERRUPT ACKNOWLEDGEMENT IN A VIRTUAL MACHINE SYSTEM	NEIGER, GILBERT
<u>10676737</u>	Not Issued	020	09/30/2003	METHOD FOR RESOLVING ADDRESS SPACE CONFLICTS BETWEEN A VIRTUAL MACHINE MONITOR AND A GUEST OPERATING SYSTEM	NEIGER, GILBERT
<u>10676584</u>	Not Issued	018	09/30/2003	INVALIDATING TRANSLATION LOOKASIDE BUFFER ENTRIES IN A VIRTUAL MACHINE (VM) SYSTEM	NEIGER, GILBERT
<u>10663206</u>	Not Issued	019	09/15/2003	USE OF MULTIPLE VIRTUAL MACHINE MONITORS TO HANDLE PRIVILEGED EVENTS	NEIGER, GILBERT
<u>10663205</u>	Not Issued	019	09/15/2003	VECTORING AN INTERRUPT OR EXCEPTION UPON RESUMING OPERATION OF A	NEIGER, GILBERT

VIRTUAL MACHINE					
<u>10663163</u>	Not Issued	019	09/15/2003	OPTIMIZING PROCESSOR-MANAGED RESOURCES BASED ON THE BEHAVIOR OF A VIRTUAL MACHINE MONITOR	NEIGER, GILBERT
<u>10322003</u>	Not Issued	020	12/17/2002	METHODS AND SYSTEMS TO CONTROL VIRTUAL MACHINES	NEIGER, GILBERT
<u>10319900</u>	Not Issued	030	12/12/2002	RECLAIMING EXISTING FIELDS IN ADDRESS TRANSLATION DATA STRUCTURES TO EXTEND CONTROL OVER MEMORY ACCESES	NEIGER, GILBERT
<u>10194822</u>	Not Issued	030	07/12/2002	SYSTEM AND METHOD FOR BINDING VIRTUAL MACHINES TO HARDWARE CONTEXTS	NEIGER, GILBERT
<u>10185123</u>	Not Issued	030	06/27/2002	INITIALIZATION OF PROTECTED SYSTEM	NEIGER, GILBERT
<u>10184798</u>	Not Issued	030	06/29/2002	HANDLING FAULTS ASSOCIATED WITH OPERATION OF GUEST SOFTWARE IN THE VIRTUAL-MACHINE ARCHITECTURE	NEIGER, GILBERT
<u>10184774</u>	Not Issued	030	06/29/2002	CONTROL OVER FAULTS OCCURRING DURING THE OPERATION OF GUEST SOFTWARE IN THE VIRTUAL-MACHINE ARCHITECTURE	NEIGER, GILBERT
<u>10165597</u>	Not Issued	030	06/07/2002	SYSTEM AND METHOD FOR PROTECTION AGAINST UNTRUSTED SYSTEM MANAGEMENT CODE BY REDIRECTING A SYSTEM MANAGEMENT INTERRUPT AND CREATING A VIRTUAL MACHINE CONTAINER	NEIGER, GILBERT
<u>10124641</u>	Not Issued	020	04/16/2002	CONTROL REGISTER ACCESS VIRTUALIZATION PERFORMANCE IMPROVEMENT IN THE VIRTUAL-MACHINE ARCHITECTURE	NEIGER, GILBERT
<u>10084282</u>	Not Issued	030	02/25/2002	METHOD AND APPARATUS FOR TRANSLATING GUEST PHYSICAL ADDRESSES IN A	NEIGER, GILBERT

VIRTUAL MACHINE ENVIRONMENT					
<u>10043843</u>	Not Issued	030	11/01/2001	APPARATUS AND METHOD FOR UNILATERALLY LOADING A SECURE OPERATING SYSTEM WITHIN A MULTIPROCESSOR ENVIRONMENT	NEIGER, GILBERT
<u>10041071</u>	Not Issued	030	12/28/2001	AUTHENTICATED CODE METHOD AND APPARATUS	NEIGER, GILBERT
<u>10039961</u>	Not Issued	030	12/31/2001	PROCESSOR SUPPORTING EXECUTION OF AN AUTHENTICATED CODE INSTRUCTION	NEIGER, GILBERT
<u>10039595</u>	Not Issued	030	12/31/2001	AUTHENTICATED CODE MODULE	NEIGER, GILBERT
<u>09931072</u>	Not Issued	030	08/15/2001	TRACKING OPERATING SYSTEM PROCESS AND THREAD EXECUTION AND VIRTUAL MACHINE EXECUTION IN HARDWARE OR IN A VIRTUAL MACHINE MONITOR	NEIGER, GILBERT
<u>09823482</u>	Not Issued	030	03/30/2001	METHOD AND APPARATUS FOR CONSTRUCTING HOST PROCESSOR SOFT DEVICES INDEPENDENT OF THE HOST PROCESSOR OPERATING SYSTEM	NEIGER, GILBERT
<u>09823131</u>	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	NEIGER, GILBERT
<u>09822986</u>	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	NEIGER, GILBERT
<u>09752586</u>	Not Issued	030	12/27/2000	MECHANISM FOR PROVIDING POWER MANAGEMENT THROUGH VIRTUALIZATION	NEIGER, GILBERT
<u>09752134</u>	Not Issued	030	12/27/2000	PROCESSOR MODE FOR LIMITING THE OPERATION OF GUEST SOFTWARE RUNNING ON A VIRTUAL MACHINE SUPPORTED BY A VIRTUAL MACHINE MONITOR	NEIGER, GILBERT
<u>09752133</u>	Not Issued	041	12/27/2000	VIRTUAL TRANSLATION LOOKASIDE BUFFER	NEIGER, GILBERT

<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GILBERT
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	NEIGER, GILBERT
<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	NEIGER, GILBERT
<u>09668610</u>	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	NEIGER, GILBERT
<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	NEIGER, GILBERT
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	NEIGER, GILBERT
<u>09618738</u>	<u>6678825</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	NEIGER, GIL
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	NEIGER, GIL
<u>09541477</u>	<u>6507904</u>	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	NEIGER, GIL
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	NEIGER, GILBERT
<u>09540946</u>	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED	NEIGER, GIL

EXECUTION					
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	NEIGER, GIL
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	NEIGER, GIL
<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	NEIGER, GIL
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	NEIGER, GIL
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GILBERT
<u>09539344</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	NEIGER, GIL
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	NEIGER, GILBERT
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	NEIGER, GILBERT

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Your Search was:

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60198227</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
<u>60198226</u>	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
<u>60198225</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C
<u>09995994</u>	6629244	150	11/16/2001	PLATFORM AND METHOD FOR ASSURING INTEGRITY OF TRUSTED AGENT COMMUNICATIONS	HERBERT, HOWARD C.
<u>09823131</u>	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	HERBERT, HOWARD C.
<u>09822986</u>	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	HERBERT, HOWARD C.
<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	HERBERT, HOWARD C.
<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	HERBERT, HOWARD C.
<u>09668610</u>	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	HERBERT, HOWARD C.

<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	HERBERT, HOWARD C.
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	HERBERT, HOWARD C.
<u>09618738</u>	<u>6678825</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	HERBERT, HOWARD C.
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
<u>09607412</u>	Not Issued	030	06/29/2000	METHOD FOR SECURE DISTRIBUTION AND CONFIGURATION OF ASYMMETRIC KEYING MATERIAL INTO SEMICONDUCTOR DEVICES	HERBERT, HOWARD C.
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	HERBERT, HOWARD C.
<u>09541477</u>	<u>6507904</u>	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	HERBERT, HOWARD C.
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	HERBERT, HOWARD C.
<u>09540946</u>	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	HERBERT, HOWARD C.
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	HERBERT, HOWARD C.
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR	HERBERT,

				GENERATING AND UTILIZING A PROTECTED AUDIT LOG	HOWARD C.
<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	HERBERT, HOWARD C.
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	HERBERT, HOWARD C.
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
<u>09539344</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	HERBERT, HOWARD C.
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	HERBERT, HOWARD C.
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	HERBERT, HOWARD C.
<u>09475806</u>	Not Issued	041	12/30/1999	PLATFORM AND METHOD FOR PERFORMANCE OPTIMIZATION OF MAGIC COOKIE COMPUTATION	HERBERT, HOWARD C.
<u>09298360</u>	<u>6389537</u>	150	04/23/1999	PLATFORM AND METHOD FOR ASSURING INTEGRITY OF TRUSTED AGENT COMMUNICATIONS	HERBERT , HOWARD C.
<u>09287782</u>	<u>6199053</u>	150	04/08/1999	DIGITAL SIGNATURE PURPOSE ENCODING	HERBERT , HOWARD C.
<u>09119682</u>	Not Issued	161	07/21/1998	TRANSFORMER	HERBERT , HOWARD
<u>09071374</u>	Not Issued	093	04/30/1998	CRYPTOGRAPHICALLY PROTECTED PAGING SUBSYSTEM	HERBERT , HOWARD C.
<u>08764154</u>	<u>5757919</u>	150	12/12/1996	CRYPTOGRAPHICALLY PROTECTED PAGING SUBSYSTEM	HERBERT , HOWARD C.
<u>08720444</u>	<u>6023509</u>	150	09/30/1996	DIGITAL SIGNATURE PURPOSE ENCODING	HERBERT , HOWARD C.
<u>08631256</u>	<u>5686677</u>	150	04/12/1996	ACOUSTIC GUITAR BRIDGE SUPPORT	HERBERT , HOWARD

<u>08625537</u>	<u>5883956</u>	150	03/28/1996	DYNAMIC CONFIGURATION OF A SECURE PROCESSING UNIT FOR OPERATIONS IN VARIOUS ENVIRONMENTS	HERBERT , HOWARD C.
<u>08485816</u>	Not Issued	161	06/07/1995	CRYPTOGRAPHIC PROCESSOR WITH MULTIPLE KEY PAIRS FOR USER AND DEVICE AUTHENTICATION	HERBERT , HOWARD C.

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Inventor Name Search Result

Your Search was:

Last Name = GOLLIVER

First Name = ROGER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60198227</u> ✓✓✓	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
<u>60198226</u> ✓✓✓	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
<u>60198225</u> ✓✓✓	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A
<u>10055406</u>	Not Issued	168	11/07/2001	COMPUTER PRODUCT AND METHOD FOR SPARSE MATRICES	GOLLIVER, ROGER
<u>09874865</u>	<u>6502117</u>	150	06/04/2001	DATA MANIPULATION INSTRUCTION FOR ENHANCING VALUE AND EFFICIENCY OF COMPLEX ARITHMETIC	GOLLIVER, ROGER A.
<u>09823131</u>	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	GOLLIVER, ROGER A.
<u>09822986</u>	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	GOLLIVER, ROGER A.
<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	GOLLIVER, ROGER A.

<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	GOLLIVER, ROGER A.
<u>09668610</u>	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	GOLLIVER, ROGER A.
<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	GOLLIVER, ROGER A.
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	GOLLIVER, ROGER A.
<u>09618738</u>	<u>6678825</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	GOLLIVER, ROGER A.
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	GOLLIVER, ROGER A.
<u>09541477</u>	<u>6507904</u>	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	GOLLIVER, ROGER A.
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	GOLLIVER, ROGER A.
<u>09540946</u>	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	GOLLIVER, ROGER A.
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	GOLLIVER, ROGER A.
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND	GOLLIVER, ROGER A.

UTILIZING A PROTECTED AUDIT LOG					
<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	GOLLIVER, ROGER A.
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	GOLLIVER, ROGER A.
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
<u>09539344</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	GOLLIVER, ROGER A.
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	GOLLIVER, ROGER A.
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	GOLLIVER, ROGER A.
<u>09223457</u>	<u>6321327</u>	150	12/30/1998	METHOD FOR SETTING A BIT ASSOCIATED WITH EACH COMPONENT OF PACKED FLOATING-PINT OPERAND THAT IS NORMALIZED IN SIMD OPERATIONS	GOLLIVER , ROGER A.
<u>09188868</u>	<u>6105047</u>	150	11/09/1998	METHOD AND APPARATUS FOR TRADING PERFORMANCE FOR PRECISION WHEN PROCESSING DENORMAL NUMBERS IN A COMPUTER SYSTEM	GOLLIVER , ROGER
<u>09183065</u>	<u>6243734</u>	150	10/30/1998	COMPUTER PRODUCT AND METHOD FOR SPARSE MATRICES	GOLLIVER , ROGER
<u>09170473</u>	<u>6272512</u>	150	10/12/1998	DATA MANIPULATION INSTRUCTION FOR ENHANCING VALUE AND EFFICIENCY OF COMPLEX ARITHMETIC	GOLLIVER , ROGER A.
<u>09170472</u>	Not Issued	161	10/12/1998	CONDITIONAL OPERAND	GOLLIVER ,

Inventor Name Search Result

Your Search was:

Last Name = ELLISON

First Name = CARL

Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
<u>60489860</u>	Not Issued	020	07/23/2003	SIMPLIFIED LIVENESS PROTOCOL	ELLISON, CARL M.
<u>60467294</u>	Not Issued	020	05/01/2003	LIVENESS PROTOCOL	ELLISON, CARL M.
<u>60198227</u> ✓ ✓ ✓	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
<u>60198226</u> ✓ ✓ ✓	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
<u>60198225</u> ✓ ✓ ✓	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M
<u>60160719</u>	Not Issued	159	10/21/1999	REAL-TIME CYTOMETRY SIGNAL TRANSFORMATION SYSTEM	ELLISON , CARL E.
<u>10678745</u>	Not Issued	019	10/03/2003	INTEGRATING A DEVICE INTO A SECURE NETWORK	ELLISON, CARL M.
<u>10608326</u>	Not Issued	020	06/27/2003	METHODS AND APPARATUS TO PROVIDE SECURE FIRMWARE STORAGE AND SERVICE ACCESS	ELLISON, CARL M.
<u>10335058</u>	Not Issued	020	12/31/2002	APPARATUS AND ASSOCIATED METHODS FOR THE SYNCHRONIZATION OF SHARED CONTENT	ELLISON, CARL
<u>10316595</u>	Not Issued	030	12/10/2002	PUBLIC KEY MEDIA KEY BLOCK	ELLISON, CARL M.
<u>10210341</u>	Not Issued	030	07/31/2002	SENSORY VERIFICATION OF SHARED DATA	ELLISON, CARL M.
<u>10111026</u>	Not Issued	030	04/18/2002	TRANSIENTLY DYNAMIC	ELLISON,

FLOW CYTOMETER ANALYSIS SYSTEM					CARL E
<u>09961380</u>	Not Issued	030	09/25/2001	AUTHENTICATED PUBLIC KEY TRANSMISSION	ELLISON, CARL M.
<u>09896088</u>	Not Issued	030	06/28/2001	TIME VARYING PRESENTATION OF ITEMS BASED ON A KEY HASH	ELLISON, CARL M.
<u>09895370</u>	Not Issued	030	06/29/2001	METHOD AND APPARATUS FOR AUTHENTICATING PEOPLE USING BUSINESS CARDS	ELLISON, CARL M.
<u>09823131</u>	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	ELLISON, CARL M.
<u>09822986</u>	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	ELLISON, CARL M.
<u>09784941</u>	Not Issued	030	02/15/2001	METHOD OF ENFORCING AUTHORIZATION IN SHARED PROCESSES USING ELECTRONIC CONTRACTS	ELLISON, CARL M.
<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	ELLISON, CARL M.
<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	ELLISON, CARL M.
<u>09668610</u>	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	ELLISON, CARL M.
<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	ELLISON, CARL M.
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	ELLISON, CARL M.
<u>09618738</u>	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.

<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	ELLISON, CARL M.
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
<u>09605605</u>	Not Issued	041	06/28/2000	PLATFORM AND METHOD FOR ESTABLISHING PROVABLE IDENTITIES WHILE MAINTAINING PRIVACY	ELLISON, CARL M.
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	ELLISON, CARL M.
<u>09541477</u>	<u>6507904</u>	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	ELLISON, CARL M.
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	ELLISON, CARL M.
<u>09540946</u>	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	ELLISON, CARL M.
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	ELLISON, CARL M.
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	ELLISON, CARL M.
<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	ELLISON, CARL M.
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	ELLISON, CARL M.
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
<u>09539344</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A	ELLISON, CARL M.

				HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	ELLISON, CARL M.
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	ELLISON, CARL M.
<u>09481747</u>	Not Issued	093	01/11/2000	SYSTEM FOR PROTECTING ANONYMITY OF PARTIES INVOLVED IN A PERSON-TO-PERSON ELECTRONIC COMMERCE TRANSACTION	ELLISON, CARL M.
<u>09452329</u>	Not Issued	041	11/30/1999	STREAM CIPHER HAVING A COMBINER FUNCTION WITH STORAGE BASED SHUFFLE UNIT	ELLISON, CARL M.
<u>09062748</u>	<u>5991406</u>	150	04/20/1998	SYSTEM AND METHOD FOR DATA RECOVERY	ELLISON , CARL M.
<u>08874459</u>	<u>5956403</u>	150	06/16/1997	SYSTEM AND METHOD FOR KEY ESCROW ENCRYPTION	ELLISON , CARL M.
<u>08781626</u>	<u>5745573</u>	150	01/10/1997	SYSTEM AND METHOD FOR CONTROLLING ACCESS TO A USER SECRET	ELLISON , CARL M.
<u>08715377</u>	<u>5640454</u>	150	09/12/1996	SYSTEM AND METHOD FOR ACCESS FIELD VERIFICATION	ELLISON , CARL M.
<u>08691564</u>	Not Issued	166	08/02/1996	SYSTEM AND METHOD FOR CONTROLLING ACCESS TO A USER SECRET	ELLISON , CARL M.
<u>08390959</u>	<u>5557765</u>	150	02/21/1995	SYSTEM AND METHOD FOR DATA RECOVERY	ELLISON , CARL M.
<u>08289602</u>	<u>5557346</u>	150	08/11/1994	SYSTEM AND METHOD FOR KEY ESCROW ENCRYPTION	ELLISON , CARL M.
<u>07898157</u>	<u>5475860</u>	150	06/15/1992	INPUT/OUTPUT CONTROL SYSTEM AND METHOD FOR DIRECT MEMORY TRANSFER ACCORDING TO LOCATION ADDRESSES PROVIDED BY THE SOURCE UNIT AND DESTINATION ADDRESSES PROVIDED BY THE DESTINATION UNIT	ELLISON , CARL

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Last Name = MITTAL

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>60437944</u>	Not Issued	020	01/02/2003	STORAGE ACCELERATION AND VIRTUALIZATION INTERFACE CARD (SAVIC) FOR SERVERS FOR IMPROVED PERFORMANCE AND SCALABILITY OF STORAGE SUB-SYSTEM	MITTAL, MILLIND
<u>60437809</u>	Not Issued	020	01/02/2003	NETWORKING ACCELERATION AND VIRTUALIZATION INTERFACE CARD (NAVIC) FOR SERVERS FOR IMPROVED PERFORMANCE AND SCALABILITY	MITTAL, MILLIND
<u>60320216</u>	Not Issued	020	05/23/2003	FIELD-PROGRAMMABLE EXTENDED FUNCTION-LEVEL-INSTRUCTION-SET COMPUTER ARCHITECTURE	MITTAL, MILLIND
<u>60249727</u>	Not Issued	159	11/17/2000	METHOD AND APPARATUS FOR PERSONAL AUTOMATIC FILING SYSTEM	MITTAL, MILLIND
<u>60198227</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
<u>10623062</u>	Not Issued	020	07/18/2003	METHOD AND APPARATUS FOR PERFORMING PACKED SHIFT OPERATIONS	MITTAL, MILLIND
<u>10611621</u>	Not Issued	019	06/30/2003	METHOD AND APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA	MITTAL, MILLIND
<u>10461782</u>	Not Issued	019	06/12/2003	METHOD FOR PROCESSING BRANCH OPERATIONS	MITTAL, MILLIND
<u>10352800</u>	Not Issued	020	01/27/2003	NETWORKED APPLICATION REQUEST SERVICING OFFLOADED FROM HOST	MITTAL, MILLIND
<u>10249416</u>	Not Issued	030	04/07/2003	NATIVE COPY INSTRUCTION FOR FILE-ACCESS PROCESSOR WITH	MITTAL, MILLIND

COPY-RULE-BASED VALIDATION					
<u>10249359</u>	Not Issued	030	04/02/2003	NATIVE LOOKUP INSTRUCTION FOR FILE-ACCESS PROCESSOR SEARCHING A THREE-LEVEL LOOKUP CACHE FOR VARIABLE-LENGTH KEYS	MITTAL, MILLIND
<u>10248029</u>	Not Issued	030	12/12/2002	FUNCTIONAL-LEVEL INSTRUCTION-SET COMPUTER ARCHITECTURE FOR PROCESSING APPLICATION-LAYER CONTENT-SERVICE REQUESTS SUCH AS FILE-ACCESS REQUESTS	MITTAL, MILLIND
<u>10185896</u>	Not Issued	030	06/27/2002	METHOD AND APPARATUS FOR UNPACKING PACKED DATA	MITTAL, MILLIND
<u>10184197</u>	Not Issued	041	06/27/2002	METHOD AND APPARATUS FOR PACKING DATA	MITTAL, MILLIND
<u>10027939</u>	Not Issued	030	12/20/2001	METHOD AND APPARATUS FOR ENABLING A LOW POWER MODE FOR A PROCESSOR	MITTAL, MILLIND
<u>09989736</u>	Not Issued	030	11/19/2001	METHOD AND APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA	MITTAL, MILLIND
<u>09785303</u>	Not Issued	093	02/16/2001	METHODS AND APPARATUSES TO CLEAR STATE FOR OPERATION OF A STACK	MITTAL, MILLIND
<u>09783816</u>	Not Issued	093	01/14/2001	PROCESSOR CAPABLE OF EXECUTING PACKED SHIFT OPERATIONS	MITTAL, MILLIND
<u>09770832</u>	Not Issued	030	01/25/2001	METHOD AND APPARATUS FOR END TO END FORWARDING ARCHITECTURE	MITTAL, MILLIND
<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
<u>09747122</u>	6631389	150	12/22/2000	PROCESSOR CAPABLE OF EXECUTING PACKED SHIFT OPERATIONS	MITTAL, MILLIND
<u>09737657</u>	Not Issued	030	12/13/2000	METHOD AND APPARATUS FOR FLEXIBLE LAYER ENCAPSULATION	MITTAL, MILLIND
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	MITTAL, MILLIND
<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	MITTAL, MILLIND
<u>09668610</u>	Not	030	09/22/2000	PROTECTING SOFTWARE	MITTAL,

	Issued			ENVIRONMENT IN ISOLATED EXECUTION	MILLIND
<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	MITTAL, MILLIND
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	MITTAL, MILLIND
<u>09657448</u>	<u>6516406</u>	150	09/08/2000	PROCESSOR EXECUTING UNPACK INSTRUCTION TO INTERLEAVE DATA ELEMENTS FROM TWO PACKED DATA	MITTAL, MILLIND
<u>09618738</u>	<u>6678825</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	MITTAL, MILLIND
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
<u>09608439</u>	Not Issued	030	06/30/2000	METHOD AND APPARATUS FOR SECURE EXECUTION USING A SECURE MEMORY PARTITION	MITTAL, MILLIND
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	MITTAL, MILLIND
<u>09541477</u>	<u>6507904</u>	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	MITTAL, MILLIND
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	MITTAL, MILLIND
<u>09540946</u>	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	MITTAL, MILLIND
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	MITTAL, MILLIND
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	MITTAL, MILLIND

<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	MITTAL, MILLIND
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	MITTAL, MILLIND
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	MITTAL, MILLIND
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	MITTAL, MILLIND
<u>09525702</u>	Not Issued	041	03/14/2000	METHOD AND APPARATUS FOR HARDWARE PLATFORM IDENTIFICATION WITH PRIVACY PROTECTION	MITTAL, MILLIND
<u>08960413</u>	<u>5983256</u>	150	10/29/1997	APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA	MITTAL , MILLIND
<u>08897283</u>	<u>5819101</u>	150	07/21/1997	METHOD FOR PACKING A PLURALITY OF PACKED DATA ELEMENTS IN RESPONSE TO A PACK INSTRUCTION	MITTAL , MILLIND
<u>08699993</u>	<u>5859997</u>	150	08/20/1996	METHOD FOR PERFORMING MULTIPLY-SUBTRACT OPERATIONS ON PACKED DATA	MITTAL , MILLIND
<u>08630876</u>	<u>5677862</u>	150	04/02/1996	A METHOD FOR MULTIPLYING PACKED DATA	MITTAL , MILLIND
<u>08626698</u>	Not Issued	166	04/02/1996	A METHOD FOR PACKING AND UNPACKING PACKED DATA	MITTAL , MILLIND
<u>08176123</u>	Not Issued	161	12/30/1993	METHOD AND APPARATUS USING PACKED DATA IN A PROCESSOR	MITTAL , MILLIND
<u>08175772</u>	Not Issued	166	12/30/1993	METHOD AND APPARATUS USING NOVEL OPERATIONS IN A PROCESSOR	MITTAL , MILLIND

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Inventor Name Search Result

Your Search was:

Last Name = LIN

First Name = DERRICK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60198227</u> ✓✓✓	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
<u>60198226</u> ✓✓	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
<u>60198225</u> ✓✓✓	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
<u>60197152</u>	Not Issued	159	04/13/2000	CRYPTOGRAPHY PROCESSING UNIT	LIN, DERRICK C.
<u>10663247</u>	Not Issued	020	09/15/2003	REGISTER STACK ENGINE HAVING SPECULATIVE LOAD/STORE MODES	LIN, DERRICK
<u>10623062</u>	Not Issued	020	07/18/2003	METHOD AND APPARATUS FOR PERFORMING PACKED SHIFT OPERATIONS	LIN, DERRICK CHU
<u>10218206</u>	Not Issued	120	08/12/2002	CLASSIFICATION ENGINE IN A CRYPTOGRAPHY ACCELERATION CHIP	LIN, DERRICK C.
<u>10218159</u>	Not Issued	120	08/12/2002	DISTRIBUTED PROCESSING IN A CRYPTOGRAPHY ACCELERATION CHIP	LIN, DERRICK C.
<u>09823131</u>	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	LIN, DERRICK C.
<u>09822986</u>	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	LIN, DERRICK C.
<u>09783816</u>	Not Issued	093	01/14/2001	PROCESSOR CAPABLE OF EXECUTING PACKED SHIFT	LIN, DERRICK CHU

OPERATIONS					
<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
<u>09747122</u>	<u>6631389</u>	150	12/22/2000	PROCESSOR CAPABLE OF EXECUTING PACKED SHIFT OPERATIONS	LIN, DERRICK CHU
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	LIN, DERRICK C.
<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	LIN, DERRICK C.
<u>09668610</u>	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	LIN, DERRICK C.
<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	LIN, DERRICK C.
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	LIN, DERRICK C.
<u>09618738</u>	<u>6678825</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	LIN, DERRICK C.
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
<u>09610798</u>	Not Issued	030	07/06/2000	DISTRIBUTED PROCESSING IN A CRYPTOGRAPHY ACCELERATION CHIP	LIN, DERRICK
<u>09610722</u>	Not Issued	030	07/06/2000	CLASSIFICATION ENGINE IN A CRYPTOGRAPHY ACCELERATION CHIP	LIN, DERRICK C.
<u>09561145</u>	<u>6631452</u>	150	04/28/2000	REGISTER STACK ENGINE	LIN, DERRICK

				HAVING SPECULATIVE LOAD/STORE MODES	
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	LIN, DERRICK C.
<u>09541477</u>	<u>6507904</u>	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	LIN, DERRICK C
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	LIN, DERRICK C.
<u>09540946</u>	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	LIN, DERRICK C.
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	LIN, DERRICK C.
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	LIN, DERRICK C.
<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	LIN, DERRICK C.
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	LIN, DERRICK C.
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
<u>09539344</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	LIN, DERRICK C.
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	LIN, DERRICK C.
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	LIN, DERRICK C.
<u>09474725</u>	Not Issued	161	12/29/1999	EAGER REGISTER STACK ENGINE	LIN, DERRICK
<u>09231426</u>	<u>6205542</u>	150	01/14/1999	PROCESSOR PIPELINE	LIN ,

INCLUDING REPLAY						DERRICK C.
<u>09132043</u>	<u>6035389</u>	150	08/11/1998	SCHEDULING INSTRUCTIONS WITH DIFFERENT LATENCIES	LIN , DERRICK C.	
<u>08998341</u>	<u>6076153</u>	150	12/24/1997	PROCESSOR PIPELINE INCLUDING PARTIAL REPLAY	LIN , DERRICK C.	
<u>08985615</u>	<u>5974525</u>	150	12/05/1997	APPARATUS AND METHOD FOR ALLOWING MULTIPLE INSTRUCTIONS TO USE THE SAME LOGICAL REGISTERS BY REMAPPING THEM TO SEPARATE PHYSICAL SEGMENT REGISTERS WHEN THE FIRST IS BEING UTILIZED	LIN , DERRICK	
<u>08898720</u>	<u>6170997</u>	150	07/22/1997	METHOD FOR EXECUTING INSTRUCTIONS THAT OPERATE ON DIFFERENT DATA TYPES STORED IN THE SAME SINGLE LOGICAL REGISTER FILE	LIN , DERRICK	
<u>08756074</u>	<u>5881279</u>	150	11/25/1996	METHOD AND APPARATUS FOR HANDLING INVALID OPCODE FAULTS VIA EXECUTION OF AN EVENT-SIGNALING MIRRO-OPERATION	LIN , DERRICK C.	
<u>08724172</u>	<u>5991884</u>	150	09/30/1996	METHOD FOR REDUCING PEAK POWER IN DISPATCHING INSTRUCTIONS TO MULTIPLE EXECUTION UNITS	LIN , DERRICK C.	
<u>08611123</u>	<u>5835782</u>	150	03/04/1996	PACKED/ADD AND PACKED SUBTRACT OPERATIONS	LIN , DERRICK C.	
<u>08610495</u>	<u>6275834</u>	150	03/04/1996	APPARATUS FOR PERFORMING PACKED SHIFT OPERATIONS	LIN , DERRICK C.	
<u>08606328</u>	<u>5959636</u>	150	02/23/1996	METHOD AND APPARATUS FOR PERFORMING SATURATION INSTRUCTIONS USING SATURATION LIMIT VALUES	LIN , DERRICK C.	
<u>08606212</u>	<u>6035316</u>	150	02/23/1996	APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA	LIN , DERRICK C.	
<u>08575704</u>	<u>5835748</u>	150	12/19/1995	METHOD FOR EXECUTING DIFFERENT SETS OF INSTRUCTIONS THAT CAUSE A PROCESSOR TO PERFORM DIFFERENT DATA TYPE OPERATIONS ON DIFFERENT	LIN , DERRICK	

				PHYSICAL REGISTERS FILES THAT LOGICALLY APPEAR TO SOFTWARE AS A SINGLE ALIASED REGISTER FILE	
08574719	5852726	150	12/19/1995	METHOD AND APPARATUS FOR EXECUTING TWO TYPES OF INSTRUCTIONS THAT SPECIFY REGISTERS OF A SHARED LOGICAL REGISTER FILE IN A STACK AND NON-STACK REFERENCED MANNER	LIN , DERRICK
08574500	5701508	150	12/19/1995	EXECUTING DIFFERENT INSTRUCTIONS THAT CAUSE DIFFERENT DATA TYPE OPERATIONS TO BE PERFORMED ON SINGLE LOGICAL REGISTER FILE	LIN , DERRICK

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Your Search was:

Last Name = MCKEEN

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 43
<u>60198227</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
<u>60198226</u>	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
<u>60198225</u>	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X
<u>10676310</u>	Not Issued	019	09/30/2003	METHOD AND SYSTEM FOR MULTIPLE BRANCH PATHS IN A MICROPROCESSOR	MCKEEN, FRANCIS
<u>10644399</u>	Not Issued	019	08/19/2003	METHOD AND APPARATUS TO RETAIN SYSTEM CONTROL WHEN A BUFFER OVERFLOW ATTACK OCCURS	MCKEEN, FRANCIS X.
<u>10639060</u>	Not Issued	019	08/11/2003	METHOD AND SYSTEM FOR AUTHENTICATING A USER OF A COMPUTER SYSTEM THAT HAS A TRUSTED PLATFORM MODULE (TPM)	MCKEEN, FRANCIS X.
<u>09823131</u>	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	MCKEEN, FRANCIS X.
<u>09822986</u>	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	MCKEEN, FRANCIS X.
<u>09751586</u>	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
<u>09705678</u>	Not Issued	041	11/02/2000	RESCHEDULING MULTIPLE	MCKEEN,

				MICRO-OPTIONS IN A PROCESSOR USING A REPLAY QUEUE	FRANCIS X.
<u>09705668</u>	Not Issued	041	11/02/2000	BREAKING REPLAY DEPENDENCY LOOPS IN A PROCESSOR USING A RESCHEDULED REPLAY QUEUE	MCKEEN, FRANCIS X.
<u>09672603</u>	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	MCKEEN, FRANCIS X.
<u>09672602</u>	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	MCKEEN, FRANCIS X.
<u>09672368</u>	Not Issued	030	09/28/2000	MECHANISM TO HANDLE EVENTS IN A MACHINE WITH ISOLATED EXECUTION	MCKEEN, FRANCIS X.
<u>09672367</u>	Not Issued	030	09/28/2000	MECHANISM TO SECURE COMPUTER OUTPUT FROM SOFTWARE ATTACK USING ISOLATED EXECUTION	MCKEEN, FRANCIS X.
<u>09668610</u>	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	MCKEEN, FRANCIS X.
<u>09668585</u>	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	MCKEEN, FRANCIS X.
<u>09668408</u>	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	MCKEEN, FRANCIS X.
<u>09618738</u>	<u>6678825</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
<u>09618659</u>	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	MCKEEN, FRANCIS X.
<u>09618489</u>	<u>6633963</u>	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION	MCKEEN, FRANCIS X.

ENVIRONMENT					
<u>09541667</u>	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	MCKEEN, FRANCIS X.
<u>09541477</u>	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	MCKEEN, FRANCIS X
<u>09541108</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	MCKEEN, FRANCIS X.
<u>09540946</u>	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	MCKEEN, FRANCIS X.
<u>09540613</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	MCKEEN, FRANCIS X.
<u>09540612</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	MCKEEN, FRANCIS X.
<u>09540611</u>	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	MCKEEN, FRANCIS X.
<u>09540608</u>	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	MCKEEN, FRANCIS X.
<u>09539348</u>	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
<u>09539344</u>	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	MCKEEN, FRANCIS X.
<u>09538954</u>	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	MCKEEN, FRANCIS X.
<u>09538951</u>	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	MCKEEN, FRANCIS X.
<u>08752729</u>	Not Issued	093	11/19/1996	MECHANISM FOR EXECUTING COMPUTER INSTRUCTIONS IN PARALLEL	MCKEEN , FRANCIS X.
<u>08339628</u>	Not Issued	166	11/15/1994	MECHANISM FOR EXECUTING	MCKEEN ,

COMPUTER INSTRUCTIONS IN PARALLEL						FRANCIS X.
08079498	5428807	150	06/17/1993	A METHOD AND APPARATUS FOR PROPAGATING EXCEPTION CONDITIONS OF A COMPUTER SYSTEM	MCKEEN , FRANCIS X.	
08079494	5420990	150	06/17/1993	MECHANISM FOR ENFORCING THE CORRECT ORDER OF INSTRUCTION EXECUTION	MCKEEN , FRANCIS X.	
08079479	Not Issued	166	06/17/1993	MECHANISM FOR EXECUTING COMPUTER INSTRUCTIONS IN PARALLEL	MCKEEN , FRANCIS X.	
08079309	5421022	150	06/17/1993	AN APPARATUS AND METHOD FOR SPECULATIVELY EXECUTING INSTRUCTIONS IN A COMPUTER SYSTEM	MCKEEN , FRANCIS X.	
08072632	6154828	150	06/03/1993	METHOD AND APPARATUS FOR EMPLOYING A CYCLE BIT TO INDICATE COMPLETION OF PARALLEL EXECUTING INSTRUCTIONS	MCKEEN , FRANCIS X.	
08061273	5285323	150	05/13/1993	INTEGRATED CIRCUIT CHIP HAVING PRIMARY AND SECONDARY RANDOM ACCESS MEMORIES FOR A HIERARCHICAL CACHE	MCKEEN , FRANCIS X.	
07638149	5113521	150	01/09/1991	METHOD AND APPARATUS FOR HANDLING FAULTS OF VECTOR INSTRUCTIONS CAUSING MEMORY MANAGEMENT EXCEPTIONS	MCKEEN , FRANCIS X.	
07093499	4980817	150	08/31/1987	VECTOR REGISTER SYSTEM FOR EXECUTING PLURAL READ/WRITE COMMANDS CONCURRENTLY & INDEPENDENTLY ROUTING DATA TO PLURAL READ/WRITE PORTS	MCKEEN , FRANCIS X.	

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3	242	((("6006328") or ("6105137") or ("6173310")) or ("6199049") or ("5025373") or ("5276878") or ("5563393") or ("5701473") or ("5745915") or ("5758149") or ("5862316") or ("5878416") or ("5893076") or ("5907801") or ("5940807") or ("5978577") or ("6014647") or ("6045039") or ("6049779") or ("6073140") or ("6076090") or ("6145738") or ("6149056") or ("6192358") or ("6260050") or ("6266651") or ("6269393") or ("6286756") or ("6320956") or ("6336137") or ("6601040") or ("6609103") or ("6622124") or ("4328544") or ("5920863") or ("5754772") or ("4274139") or ("4785408") or ("4799156") or ("4903200") or ("4969183") or ("5367639") or ("5577105") or ("5724556") or ("5761281") or ("5806074") or ("5826267") or ("5841842") or ("5872971") or ("5897634") or ("5999942") or ("6070190") or ("6070177") or ("6119104") or ("6125391") or ("6128611") or ("6141660") or ("6141699") or ("6208992") or ("6226675") or ("6226675") or ("6272675") or ("6360229") or ("6401111") or ("6446045") or ("6473807") or ("6625602") or ("5404508") or ("5629981") or ("3798605") or ("4251009") or ("4295039") or ("4351247") or ("4380201") or ("4386266") or ("4407146") or ("4616852") or ("4864616") or ("5001752") or ("5205224") or ("5267314") or ("5317137") or ("5473144") or ("5524073") or ("5548106") or ("5555303") or ("5613007") or ("5646998") or ("5710889") or ("5726430") or ("5732878") or ("5742034") or ("5793302") or ("5794221") or ("5819263") or ("5824571") or ("5842043") or ("5870721") or ("5883371") or ("5884312") or ("5890140") or ("5915008") or ("5923759") or ("5936541") or ("5963647") or ("5974148") or ("5970888") or ("6012067") or ("6018724") or ("6058378") or ("6064988") or ("6097306") or ("6098053") or ("6105131") or ("6141752") or ("6148405") or ("6188994") or ("6202159") or ("6230148") or ("6269348") or ("6283366").PN. secur\$5 or protect\$ or integrit\$ or guard\$ or valid\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 13:53
5	3474037		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:03
6	7577342	transaction\$ or access\$5 or exchang\$ or input\$ or output\$ or translation\$ or entry or enter\$ or exit\$ or enabl\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 13:57
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16	5487144	physical or mask or base or length	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:03

17	758	((("6505279") or ("5079737") or ("5628022") or ("6192455") or ("6085296") or ("6148379") or ("6499123") or ("6339816") or ("6125430") or ("5852717") or ("5386552") or ("4037214") or ("6249872") or ("6055637") or ("5757919") or ("5737760") or ("6292874") or ("6272533") or ("6339815") or ("5872994") or ("6301646") or ("6321314") or ("20010027527") or ("EP930567A") or ("WO9834365A") or ("4307214") or ("4366537") or ("5075842") or ("5890189") or ("5987557") or ("5255379") or ("5459869") or ("5615263") or ("5717903") or ("5764969") or ("2001003745") or ("4278837") or ("4521852") or ("5022077") or ("5293424") or ("5421006") or ("5473692") or ("5479509") or ("5568552") or ("5657445") or ("5729760") or ("5796845") or ("5805712") or ("5835594") or ("5844986") or ("5937063") or ("5978481") or ("6014745") or ("6058478") or ("6158546") or ("6175925") or ("4319323") or ("4759064") or ("4795893") or ("5007082") or ("5293424") or ("5317705") or ("5459867") or ("5504922") or ("5606617") or ("5901225") or ("5970147") or ("6061794") or ("6092095") or ("6115816") or ("6212635") or ("6222923") or ("6269392") or ("6330670") or ("6357004") or ("6363485") or ("6374286") or ("6378072") or ("6389537") or ("6412035") or ("6421702") or ("6445797") or ("6463537") or ("6535988") or ("6557104") or ("6633981") or ("DE4217444") or ("EP473913") or ("4571672") or ("5455909") or ("5953502") or ("6101584") or ("6633963") or ("6292569") or ("6507904") or ("6289462") or ("6157721") or ("5421006") or ("5361359") or ("5657445") or ("5493279") or ("6088451") or ("6253193") or ("6332135") or ("6336105") or ("6338050") or ("6363488") or ("6389402") or ("6427140") or ("6182227") or ("5185692") or ("5542044") or ("5276735") or ("5499297") or ("5502766") or ("5533123") or ("6138239") or ("6233576") or ("4926476") or ("6192512") or ("5892903") or ("6272533") or ("6430561") or ("6453345") or ("6405318") or ("5648648") or ("5910988") or ("6032137") or ("4624600") or ("5392357") or ("6038551") or ("6070242") or ("6266413").PN. or ((2001/0021969) or (WO97/29567) or (WO01/75565) or (WO01/27723) or (WO01/27527)).CCLS.) or (((("6289455") or ("6477651") or ("4432057") or ("5535386") or ("5796999") or ("5893117") or ("6321263") or ("4941090") or ("5202826") or ("5287268") or ("5291003") or ("5764742") or ("5796832") or ("5870733") or ("RE36116") or ("6002605") or ("6005939") or ("6128602") or ("6137710") or ("6308277") or ("6611810") or ("6067618") or ("5280619") or ("5534855") or ("5751956") or ("5801364") or ("5812991") or ("5844218") or ("5870546") or ("6023684") or ("6052781") or ("6061057") or ("6134591") or ("6141006") or ("6189030") or ("6385724") or ("6466966") or ("6477559") or ("5428795") or ("5482649") or ("4422287") or ("6134661") or ("5301231") or ("5343529") or ("6468160") or ("5265163") or ("5289540"))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:03
Search History	1/9/04 3:10:57 PM	Page 1 of 1 C:\APPS\EAST\Workspaces\best10009		

18	445999	((secur\$5 or protect\$ or integrit\$ or guard\$ or valid\$) with (transaction\$ or access\$5 or exchang\$ or input\$ or output\$ or translation\$ or entry or enter\$ or exit\$ or enabl\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:05
19	2481906	(information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration\$ or set\$4 or architectur\$ or structur\$ or design\$ or count\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:09
20	407122	((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration\$ or set\$4 or architectur\$ or structur\$ or design\$ or count\$5)) with (stor\$5 or cach\$5 or memory or register\$ or spreadsheet\$ or databas\$ or buffer\$ or bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:26
21	895924	(isolated or external or normal or logical or state or separate) with (execution\$ or task\$ or program\$ or combin\$ or generat\$ or updat\$ or initial\$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:27
22	389222	(address or value) with (physical or mask or base or length)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:28
23	36278	(detector\$ or sensor\$ or monitor\$ or check\$ or snoop\$ or manager\$) with ((address or value) with (physical or mask or base or length))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:29
24	17426	((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration\$ or set\$4 or architectur\$ or structur\$ or design\$ or count\$5)) with ((secur\$5 or protect\$ or integrit\$ or guard\$ or valid\$) with (transaction\$ or access\$5 or exchang\$ or input\$ or output\$ or translation\$ or entry or enter\$ or exit\$ or enabl\$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:31
25	4863	((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration\$ or set\$4 or architectur\$ or structur\$ or design\$ or count\$5)) with ((secur\$5 or protect\$ or integrit\$ or guard\$ or valid\$) with (transaction\$ or access\$5 or exchang\$ or input\$ or output\$ or translation\$ or entry or enter\$ or exit\$ or enabl\$5)) with (((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration\$ or set\$4 or architectur\$ or structur\$ or design\$ or count\$5)) with (stor\$5 or cach\$5 or memory or register\$ or spreadsheet\$ or databas\$ or buffer\$ or bus))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:32

27	1	(((((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration\$ or set\$4 or architectur\$ or structur\$ or design\$ or count\$5)) with ((secur\$5 or protect\$ or integrit\$ or guard\$ or valid\$) with (transaction\$ or access\$5 or exchang\$ or input\$ or output\$ or translation\$ or entry or enter\$ or exit\$ or enabl\$5))) with (((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration\$ or set\$4 or architectur\$ or structur\$ or design\$ or count\$5)) with (stor\$5 or cach\$5 or memory or register\$ or spreadsheet\$ or databas\$ or buffer\$ or bus))) with ((detector\$ or sensor\$ or monitor\$ or check\$ or snoop\$ or manager\$) with ((address or value) with (physical or mask or base or length)))) and (((("6505279") or ("5079737") or ("5628022") or ("6192455") or ("6085296") or ("6148379") or ("6499123") or ("6339816") or ("6125430") or ("5852717") or ("5386552") or ("4037214") or ("6249872") or ("6055637") or ("5757919") or ("5737760") or ("6292874") or ("6272533") or ("6339815") or ("5872994") or ("6301646") or ("6321314") or ("20010027527") or ("EP930567A") or ("WO9834365A") or ("4307214") or ("4366537") or ("5075842") or ("5890189") or ("5987557") or ("5255379") or ("5459869") or ("5615263") or ("5717903") or ("5764969") or ("2001003745") or ("4278837") or ("4521852") or ("5022077") or ("5293424") or ("5421006") or ("5473692") or ("5479509") or ("5568552") or ("5657445") or ("5729760") or ("5796845") or ("5805712") or ("5835594") or ("5844986") or ("5937063") or ("5978481") or ("6014745") or ("6058478") or ("6158546") or ("6175925") or ("4319323") or ("4759064") or ("4795893") or ("5007082") or ("5293424") or ("5317705") or ("5459867") or ("5504922") or ("5606617") or ("5901225") or ("5970147") or ("6061794") or ("6092095") or ("6115816") or ("6212635") or ("6222923") or ("6269392") or ("6330670") or ("6357004") or ("6363485") or ("6374286") or ("6378072") or ("6389537") or ("6412035") or ("6421702") or ("6445797") or ("6463537") or ("6535988") or ("6557104") or ("6633981") or ("DE4217444") or ("EP473913") or ("4571672") or ("5455909") or ("5953502") or ("6101584") or ("6633963") or ("6292569") or ("6507904") or ("6289462") or ("6157721") or ("5421006") or ("5361359") or ("5657445") or ("5493279") or ("6088451") or ("6253193") or ("6332135") or ("6336105") or ("6338050") or ("6363488") or ("6389402") or ("6427140") or ("6182227") or ("5185692") or ("5542044") or ("5276735") or ("5499297") or ("5502766") or ("5533123") or ("6138239") or ("6233576") or ("4926476") or ("6192512") or ("5892903") or ("6272533") or ("6430561") or ("6453345") or ("6405318") or ("5648648") or ("5910988") or ("6032137") or ("4624600") or ("532357") or ("6038551") or ("6070242") or ("6266413"))).PN. or C:\APPS\EAST\Workspaces\test\00100988 or (WO97/29567) or (WO01/75565) or (WO01/27723) or	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:34
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26	20	((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration\$ or set\$4 or architectur\$ or structur\$ or design\$ or count\$5)) with ((secur\$5 or protect\$ or integrat\$ or guard\$ or valid\$) with (transaction\$ or access\$5 or exchang\$ or input\$ or output\$ or translation\$ or entry or enter\$ or exit\$ or enabl\$5)) with (((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration\$ or set\$4 or architectur\$ or structur\$ or design\$ or count\$5)) with (stor\$5 or cach\$5 or memory or register\$ or spreadsheet\$ or databas\$ or buffer\$ or bus))) with ((detector\$ or sensor\$ or monitor\$ or check\$ or snoop\$ or manager\$) with ((address or value) with (physical or mask or base or length)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:35
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